DIGITAL SIGNAL PROCESSING
(Elective – III)

Preamble:

Signals analysis is very important in daily life. Hence it is required to study the different signals (continuous and discrete) and their properties. The behavior of the signals in time and frequency domain are important in analyzing the response of the network. The tools like FFT, DFT, Z–transforms may be used in the analysis of the signals. Filters must be required to eliminate the unwanted signals. Hence digital filter design also required to be studied. Sampling of signals are required to convert continuous to discrete signals. To have knowledge on the implementation signals, DSP processors must be studied.

Learning Objectives:

- To study different types of signals and properties of systems.
- To study the application of Fourier transform to discrete time systems.
- To study the FFT and inverse FFT and its applications to discrete sequences.
- To study the realization of digital filters and their design.
- To study the multi–rate signal processing.
- To study the architecture of digital signal processors.

UNIT–I:

Introduction

Introduction to Digital Signal Processing**:** Discrete time signals & sequences – Linear shift invariant systems – Stability and causality – Linear constant coefficient difference equations.

UNIT–II:

Discrete Fourier Series

Properties of discrete Fourier series, DFS representation of periodic sequences, Discrete Fourier transforms: Properties of DFT, linear convolution of sequences using DFT, Computation of DFT. Relation between Z–transform and DFS.

UNIT–III:

Fast Fourier Transforms

Frequency domain representation of discrete time signals and systems – Fast

Fourier transforms (FFT) – Radix–2 decimation in time and decimation in frequency FFT Algorithms – Inverse FFT – and FFT for composite N.

UNIT–IV:

Realization of Digital Filters

Solution of difference equations of digital filters – Block diagram representation of linear constant – Coefficient difference equations – Basic structures of IIR systems – Transposed forms – Basic structures of FIR systems – System function.

IIR Digital Filters

Analog filter approximations – Butter worth and Chebyshev – Design of IIR Digital filters from analog filters – Design Examples**:** Analog–Digital transformations.

FIR Digital Filters

Characteristics of FIR Digital Filters – Frequency response – Design of FIR Digital Filters using Window Techniques – Frequency Sampling technique – Comparison of IIR & FIR filters.

UNIT–V:

Multirate Digital Signal Processing:

Decimation – Interpolation – Down sampling – Up sampling rate – Conversion – Implementation of sampling rate conversion.

UNIT–VI:

Introduction to Digital Signal Processors(DSP):

Introduction to programmable DSPs**:** Multiplier and Multiplier Accumulator (MAC) – Modified bus structures and memory access schemes in DSPs – Multiple access memory – Multiport memory – VLSI architecture – Pipelining – Special addressing modes – On–chip peripherals – Architecture of TMS 320C5X – Introduction – Bus structure – Central arithmetic logic unit – Auxiliary registrar – Index registrar – Auxiliary register compare register – Block move address register – Parallel logic unit – Memory mapped registers – Program controller – Some flags in the status registers – On–chip registers, On–chip peripherals.

Learning outcomes:

- Able to study different types of signals and properties of systems.
- Able to apply of Fourier transform to discrete time systems.
- Able to apply the FFT and inverse FFT to discrete sequences.

- Able to realize and design digital filters.
- Able to understand the multi–rate signal processing.
- Able to understand architecture of digital signal processors.

Text Books:

1. Digital Signal Processing – Alan V. Oppenheim, Ronald W. Schafer, PHI Ed., 2006
2. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G. Manolakis, Pearson Education / PHI, 2007

Reference Books:

1. Digital Signal Processing: Andreas Antoniou, TATA McGraw Hill , 2006
2. Digital Signal Processing: MH Hayes, Schaum's Outlines, TATA Mc–Graw Hill, 2007.
3. DSP Primer – C. Britton Rorabaugh, Tata Mc Graw Hill, 2005.
4. Fundamentals of Digital Signal Processing using Matlab – Robert J. Schilling, Sandra L. Harris, Thomson, 2007.
5. Digital Signal Processors – Architecture, Programming and Applications, B.Venkataramani, M.Bhaskar, TATA McGraw Hill, 2002.